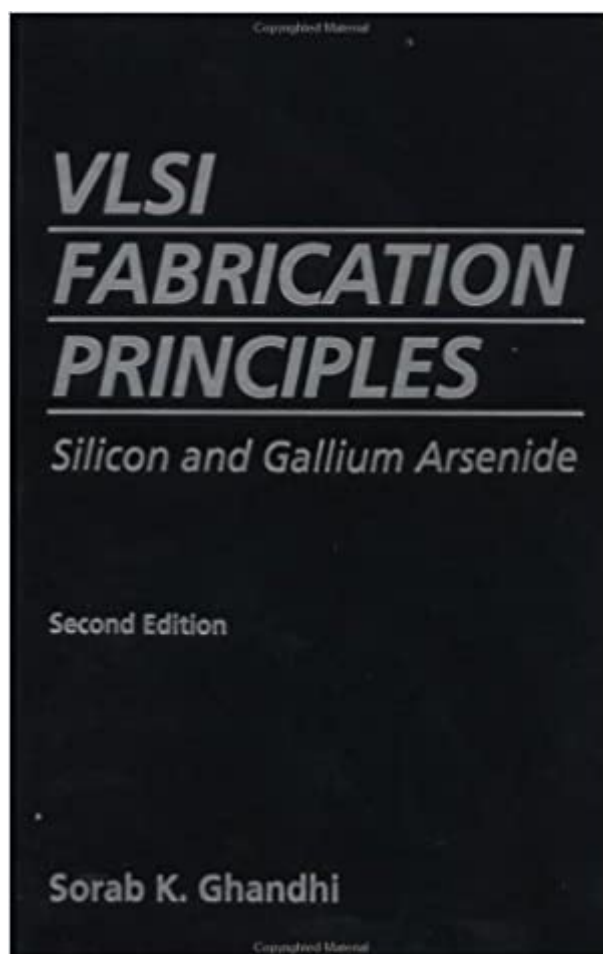


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VLSI Fabrication Principles: Silicon And Gallium Arsenide, 2nd Edition



Synopsis

Fully updated with the latest technologies, this edition covers the fundamental principles underlying fabrication processes for semiconductor devices along with integrated circuits made from silicon and gallium arsenide. Stresses fabrication criteria for such circuits as CMOS, bipolar, MOS, FET, etc. These diverse technologies are introduced separately and then consolidated into complete circuits. An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department.

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Customer Reviews

Fully updated with the latest technologies, this edition covers the fundamental principles underlying fabrication processes for semiconductor devices along with integrated circuits made from silicon and gallium arsenide. Stresses fabrication criteria for such circuits as CMOS, bipolar, MOS, FET, etc. These diverse technologies are introduced separately and then consolidated into complete circuits.

Book was in Good Condition and was an interesting textbook. I would recommend it for material scientists and engineers or those interested in these fabrication principles.

That's not very great book. There is some useful graphs in it, but you can find those in any semiconductor related book. The rest is useless.

I have been a student of semiconductor device physics and processing since 1985, and now a professor of electrical engineering teaching the subject since 2000. I have utilized many textbooks on the subject over the past 27 years (I have explored more than 16 texts for instruction) but find myself returning over and over again to this second edition of S. Ghandi's text because it covers the fundamentals with depth so well. While it no longer represents the state of the art in terms of technological discussion in this nanoscaled device age, it is timeless in its presentation of the foundational materials and core behaviors and interactions of materials in semiconductor devices. There simply is not a better presentation and organization of this material available, nor are there better derivations to detail the concepts. The book has its flaws: it can be wordy and does contain errors in some derivations and more than one problem set, but the benefits more than outweigh the flaws. There are some more focused reference texts that dig even deeper in some materials and materials processing aspects presented in Ghandi, but this book represents a core value for a 1st graduate course overviewing semiconductor fabrication and is a great partner to Runyan & Bean, an excellent Sr. level book. It is my "go-to" resource for lectures, and is the book I choose when I teach semiconductor fabrication.

This is an excellent reference book for these days. I like this book.

This book provides some valuable information on the processing of integrated circuits.

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